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1763
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TRANSMITTAL FORM

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Application Number	08/988,246
Filing Date	December 1, 1997
First Named Inventor	Raoux, Sebastien
Art Unit	1763
Examiner Name	Rudy Zervigon
Attorney Docket Number	A1771/T19930

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

☐ Fee Transmittal Form

☐ Fee Attached

☒ Amendment/Reply

☐ After Final

☐ Affidavits/declaration(s)

☐ Extension of Time Request

☐ Express Abandonment Request

☐ Information Disclosure Statement

☐ Certified Copy of Priority Document(s)

☐ Response to Missing Parts/
Incomplete Application

☐ Response to Missing Parts
under 37 CFR 1.52 or 1.53

☐ Drawing(s)

☐ Licensing-related Papers

☐ Petition

☐ Petition to Convert to a
Provisional Application

☐ Power of Attorney, Revocation
Change of Correspondence Address

☐ Terminal Disclaimer

☐ Request for Refund

☐ CD, Number of CD(s)

☐ After Allowance Communication to Group

☐ Appeal Communication to Board of Appeals
and Interferences

☐ Appeal Communication to Group (Appeal
Notice, Brief, Reply Brief)

☐ Proprietary Information

☐ Status Letter

☒ Other Enclosure(s)
(please identify below)

Two Return Postcards

Remarks

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm
or
Individual

Townsend and Townsend and Crew LLP
Chun-Pok Leung

Reg. No. 41,405

Signature

Chun-Pok Leung

Date

5-8-03

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May 8, 2003

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PATENT
Attorney Docket No.: A1771/T19930
TTC Ref. No.: 016301-019930

On May 8, 2003

TOWNSEND and TOWNSEND and CREW LLP

By: Andrea

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TC 1700

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SEBASTIEN RAOUX

Application No.: 08/988,246

Filed: December 1, 1997

For: METHOD AND APPARATUS FOR
MONITORING AND ADJUSTING
CHAMBER IMPEDANCE

Examiner: Rudy Zervigon

Art Unit: 1763

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed February 26, 2003, please reconsider the above-identified application in view of the following comments.

IN THE CLAIMS:

The claims are unamended, but are reproduced below for the Examiner's convenience and reference.

3. The substrate processing system of claim 11 wherein said processor receives as an input the measured impedance level of said plasma.

4. The substrate processing system of claim 3 further comprising a variable capacitor electrically coupled to said chamber and controllably coupled to said